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second counter which starts counting a number of horizontal and vertical delays of said image signals at said filter, beginning when said first counter starts counting; a third counter which starts counting the number of horizontal and vertical delays of said image signals at said filter, after said first counter has finished counting; and a second flip-flop which generates said signals controlling reading after said second counter has finished counting, and until a time period when said third counter finishes counting.

- 11. The device according to claim 7, wherein said IIR type filter is at least one of a low-pass filter and an all-pass filter.
- 12. The device according to claim 6, wherein said luminance image signal generator is a matrix calculator.
- 13. The device according to claim 6, wherein said dynamic range compressor subjects to said dynamic range compression treatment using a dynamic range compression table. --_____

CONCLUSION

Accordingly, in view of the above amendments and remarks, reconsideration of the objections and rejections and allowance of each of claims 6-13 in connection with the present application is earnestly solicited.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Donald J. Daley, Reg. No. 34,313 at the telephone number of the undersigned below.

DJD/MJL:kna

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If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

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